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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Katsushi NAGABA and Shigeo OHSHIMA

Serial No: Not Assigned

Filed: September 25, 2001

For: Semiconductor Integrated Circuit
Device

Art Unit: Not Assigned

Examiner: Not Assigned

**TRANSMITTAL OF INFORMATION DISCLOSURE
STATEMENT**

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

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Date

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: September 25, 2001

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